

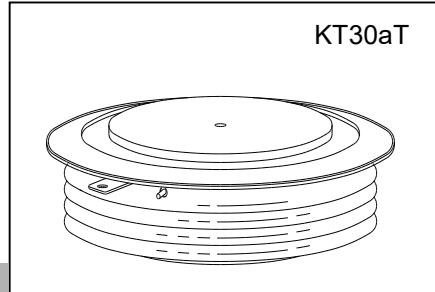
**R A E**

Jiangsu Runau Electronics Manufacturing Co.,Ltd

YC430-Power Thyristor500-1600V_{DRM}

HIGH POWER THYRISTOR FOR PHASE CONTROL APPLICATIONS**Features:**

- . All Diffused Structure
- . Interdigitated Amplifying Gate Configuration
- . Blocking capability up to 1600 volts
- . Guaranteed Maximum Turn-Off Time
- . High dV/dt Capability
- . Pressure Assembled Device

**ELECTRICAL CHARACTERISTICS AND RATINGS****Blocking - Off State**

Device Type	V _{RRM} (1)	V _{DRM} (1)	V _{RSM} (1)
YC430E	500	500	600
YC430M	600	600	720
YC430N	800	800	960
YC430P	1000	1000	1150
YC430PB	1200	1200	1300
YC430PD	1400	1400	1500
YC430PM	1600	1600	1700

V_{RRM} = Repetitive peak reverse voltageV_{DRM} = Repetitive peak off state voltageV_{RSM} = Non repetitive peak reverse voltage (2)

Repetitive peak reverse leakage and off state leakage	I _{RRM} / I _{DRM}	15 mA 35 mA (3)
Critical rate of voltage rise	dV/dt (4)	200 V/μsec

Notes:

All ratings are specified for T_j=25 °C unless otherwise stated.

(1) All voltage ratings are specified for an applied 50Hz/60Hz sinusoidal waveform over the temperature range -40 to +125 °C.

(2) 10 msec. max. pulse width

(3) Maximum value for T_j = 125 °C.(4) Minimum value for linear and exponential waveshape to 80% rated V_{DRM}. Gate open. T_j = 125 °C.

(5) Non-repetitive value.

(6) The value of di/dt is established in accordance with EIA/NIMA Standard RS-397, Section 5-2-2-6. The value defined would be in addition to that obtained from a snubber circuit, comprising a 0.2 μF capacitor and 20 ohms resistance in parallel with the thristor under test.

Conducting - on state

Parameter	Symbol	Min	Max	Typ	Units	Conditions
Average value of on-state current	I _{T(AV)}		700		A	Sinewave, 180° conduction, T _c =65°C
RMS value of on-state current	I _{TRMS}		1000		A	Nominal value
Peak one cycle surge (non repetitive) current	I _{TSM}		8000		A	8.3 msec (60Hz), sinusoidal wave- shape, 180° conduction, T _j = 125 °C
			7400		A	10.0 msec (50Hz), sinusoidal wave- shape, 180° conduction, T _j = 125 °C
I square t	I ² t		265000		A ² s	8.3 msec
Latching current	I _L		800		mA	V _D = 24 V; R _L = 12 ohms
Holding current	I _H		400		mA	V _D = 24 V; I = 2.5 A
Peak on-state voltage	V _{TM}		2.20		V	I _{TM} = 2000 A;
Critical rate of rise of on-state current (5, 6)	di/dt		400		A/μs	Switching from V _{DRM} ≤ 1000 V, non-repetitive
Critical rate of rise of on-state current (6)	di/dt		150		A/μs	Switching from V _{DRM} ≤ 1000 V

Gating

Parameter	Symbol	Min	Max	Typ	Units	Conditions
Peak gate power dissipation	P _{GM}		200		W	t _p = 40 us
Average gate power dissipation	P _{G(AV)}		5		W	
Peak gate current	I _{GM}		10		A	
Gate current required to trigger all units	I _{GT}		300 200 125		mA	V _D = 6 V; R _L = 3 ohms; T _j = -40 °C V _D = 6 V; R _L = 3 ohms; T _j = +25 °C V _D = 6 V; R _L = 3 ohms; T _j = +125°C
Gate voltage required to trigger all units	V _{GT}	0.30	5 3		V	V _D = 6 V; R _L = 3 ohms; T _j = -40 °C V _D = 6 V; R _L = 3 ohms; T _j = 0-125°C V _D = Rated V _{DRM} ; R _L = 1000 ohms; T _j = + 125 °C
Peak negative voltage	V _{GRM}		5		V	

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Delay time	t _d		1.5	0.7	μs	I _{TM} = 50 A; V _D = 67% V _{DRM} Gate pulse: V _G = 30 V; R _G = 10 ohms; t _r = 0.1 μs; t _p = 20 μs
Turn-off time (with V _R = -5 V)	t _q		250	125	μs	I _{TM} > 1000 A; di/dt = 25 A/μs; V _R ≥ -5 V; Re-applied dV/dt = 20V/μs linear to 67% V _{DRM} ; T _j = 125 °C; Duty cycle ≥ 0.01%
Reverse recovery charge	I _{rr}				μC	I _{TM} > 1000 A; di/dt = 25 A/μs; V _R ≥ -50 V; T _j = 125 °C

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T _j	-40	+125		°C	
Storage temperature	T _{stg}	-40	+150		°C	
Thermal resistance - junction to case	R _{Θ(j-c)}		0.055 0.110		°C/W	Double sided cooled Single sided cooled
Thermal resistance - case to sink	R _{Θ(c-s)}		0.030 0.060		°C/W	Double sided cooled * Single sided cooled
Mounting force	P	800 3.6	2500 11.2		lb. kN	
Weight	W			2.5 70	oz g.	

* Mounting surfaces smooth, flat and greased

